

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	A Command Multiplier for Built-In-Self-Test
--------------------	---------------------------------------------

Application Number :

Confirmation Number:

First Named Applicant: Jonathan Fales

Attorney Docket Number: BUR920040037US1

Art Unit:

Examiner:

Search string: (6647524 or 6583642 or 6574760 or 6611933 or 6360342 or 6055658 or 5614838 or 4903266 or 6550033 or 6001662 or 5954830 or 5388104).pn

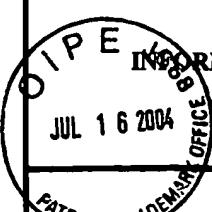
US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
CB	1	6647524	2003-11-11	Huang, et al		714	734
CB	2	6583642	2003-06-24	Huang, et al		324	765
CB	3	6574760	2003-06-03	Mydill		714	724
CB	4	6611933	2003-08-26	Koenemann, et al.		714	726
CB	5	6360342	2002-03-19	Lee, et al.		714	718
CB	6	6055658	2000-04-25	Jaber, et al.		714	726
CB	7	5614838	1997-03-25	Jaber, et al.		324	765
CB	8	4903266	1990-02-20	Hack		371	21.2
CB	9	6550033	2003-04-15	Dwork		714	733
CB	10	6001662	1999-12-14	Correale, Jr. et al.		438	11
CB	11	5954830	1999-09-21	Ternullo, Jr.		714	718
CB	12	5388104	1995-02-07	Shirotori, et al.		371	21.1

Signature

Examiner Name	Date
/Cynthia Britt/	06/22/2006



INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

JUL 16 2004

Docket Number (Optional) BUR920040037US1	Application Number 10/708184
Applicant(s) FALES ET AL.	
Filing Date 02/13/04	Group Art Unit

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
CB		JP2003229000A	06/26/03	JAPAN				
CB		JP7078495A	03/20/95	JAPAN				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

CB		"A 800MHz Embedded DRAM with a Concurrent Refresh Mode", Toshiaki Kirihata et al., IBM Microelectronics, Hopewell Junction, NY, pp. 1-6.
CB		"Fault Simulation on the YSE/EVE", Barzilai et al., IBM Technical Disclosure Bulletin, Vol. 28, No. 2, July 1985, pp.773-776.

EXAMINER /Cynthia Britt/ DATE CONSIDERED 06/22/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.